

(Use several sheets if necessary.)

NEG-332US

Application Number 10/785, 015
~~Not Yet Assigned~~

Toru Ishikawa

2/25/04

~~Concurrently Herewith~~

2816

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Copy		Tatsuya Matano, et al., "A 1-Gb/pin 512-Mb DDRII SDRAM using a digital DLL and a slew-rate-controlled output buffer", 2002 VLSI Symposium: Thesis number 9-1.

Love to you

07/26/2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.